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A semiconductor isolation structure comprising:

a substrate, the substrate comprising a surface;

a first device and a second device formed within the substrate;

an isolation region formed within the substrate between the first device and the second device, the isolation region comprising:

a deep region which extends into the substrate, the deep region comprising a deep region cross-sectional area;

a shallow region which extends to the surface of the substrate, the shallow region comprising a shallow region cross-sectional area; wherein

the deep region cross-sectional area is greater than the shallow region cross-sectional area.

- 2. The semiconductor isolation structure as recited in claim 1, wherein the isolation region comprises an oxide.
- 3. The semiconductor isolation structure as recited in claim 1, wherein the shallow region comprises a protective outer walk adjacent to the substrate.
- The semiconductor isolation structure as recited in claim 1, wherein the protective outer wall comprises a layer of Nitride.

5. A semiconductor isolation structure comprising:

8. The method of forming an isolation structure within a substrate of claim 7, wherein the step of 1 forming a protective wall within the trench comprises: 2 growing an oxide layer on a surface of the trench; and 3 depositing a nitride layer over the xide layer. 4 9. The method of forming an isolation structure within a substrate of claim 7, wherein the step of 1 removing a bottom portion of the protective wall layer exposing a surface of the substrate 2 comprises: 3 removing a bottom portion of the protective wall layer exposing the substrate; and forming a second trench in the exposed substrate forming an exposed surface.